

PIC Special and reserved I/O Lines									PicKit2 Connections		
	16F84 / (16C71/710)		16F627/8		12F629 / (12F675)		16F870			16F871	
		Pin/18		Pin/18		Pin/8		Pin/28		Pin/40	
PGM Clock PGC	RB6	11	RB6	11	GP1	6	RB6	27	RB6	39	5
PGM Data PGD	RB7	12	RB7	12	GP0	7	RB7	28	RB7	40	4
MCLR/Vpp (if optional)		4	RA5	4	GP3	4		1	-	1	1
OSC1/ClkIn (if optional)		16	RA7	16	GP5	2		9		13	Vdd = 2
OSC2/ClkOut (if optional)		15	RA6	15	GP4	3		10		14	GND = 3
PGM / LV PGM			RB4	10			RB3	24	RB3	36	
T0CKI	RA4	3	RA4	3	GP2	5	RA4	6	RA4	6	
T1CKI			RB6	11	GP5	2	RC0	11	RC0	15	
T1OSI			RB7	12			RC1	12	RC1	16	
CCPI			RB3	9			RC2	13	RC2	17	
Analog0 (A/D or Comp)	(RA0)	17	RA0	17	(GP0)	7	RA0	2	RA0	2	
Analog1 "	(RA1)	18	RA1	18	(GP1)	6	RA1	3	RA1	3	
Analog2 "	(RA2)	1	RA2	1	(GP2)	5	RA2	4	RA2	4	
Analog3 "	(RA3)	2	RA3	2	(GP4)	3	RA3	5	RA3	5	
Analog4 "							RA5	7	RA5	7	
Analog5 "									RE0	8	
Analog6 "									RE1	9	
Analog7 "									RE2	10	
Analog Reference -ve							RA2	4	RA2	4	
Analog Reference +ve	(RA3)	2	RA2	1	(GP1)	6	RA3	5	RA3	5	
CMP1			RA3	2							
CMP2			RA4	3							
Serial Input (RXD)			RB1	7			RC7	18	RC7	26	
Serial Output (TXD)			RB2	8			RC6	17	RC6	25	